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- Ph.D., EECS, UC Berkeley, 2004; B.S., M.S., Electronics Engineering, National Chiao Tung University, 1996, 1998
- Research interests
 - Synthesis
 - Logic synthesis, RTL synthesis, asynchronous circuit synthesis
 - Verification
 - Design rectification, circuit/function identification
 - Quantified decision procedures
 - QBF, SSAT, DQBF, DSSAT, SOQBF
 - Computation models
 - Quantum computation, computation with biochemistry

